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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)			Application Number	TBD	
			Filing Date	August 26, 2003	
			First Named Inventor	Kelvin Ma, et al.	
			Group Art Unit	TBD	
			Examiner Name	TBD	
Sheet	1	of	1	Attorney Docket Number	TA-00633

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	A	KIRK, A.G., et al., "A Multistage Free-Space Optical Interconnect for Backplane Applications: Implementation Issues," April, 1998, Institute of Electrical and Electronic Engineers, pgs. 149-150.	
	B	ROBERTSON, Brian, "Design of an Optical Interconnect for Photonic Backplane Applications," Applied Optics, Vol. 37, No. 14, May 10, 1998, pgs. 2974-2984.	
	C	World Wide Web, <a href="http://www.oe.uni-duisburg.de/Reports/jb95/mod1.html">http://www.oe.uni-duisburg.de/Reports/jb95/mod1.html</a> , HEINZELMANN, R., "Electro-optic Modulators for Hybrid Integration," printed on April 23, 2003.	
	D	World Wide Web, <a href="http://www.ecacs.uc.edu/~psdl/projects/WaveguideTaps/WaveguideTaps.htm">http://www.ecacs.uc.edu/~psdl/projects/WaveguideTaps/WaveguideTaps.htm</a> , "Optical Waveguide Taps on Silicon CMOS Circuits," STENGER, Vincent et al., printed 04/23/03.	
	E	BOGALECKI, Alfons W., "Design of a Waveguide Based Optoelectronic Integrated Circuit in Silicon 0.8 μm BiCMOS Technology," 2000, 4 pgs.	

Examiner Signature		Date Considered	11/24/04
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